



Substitute for form 1449A/PTO		Complete if Known			
		Application / Conf. No.	10/805,116 / 8509		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Filing Date	March 19, 2004		
		First Named Inventor	Eric J. Crabill		
		Art Unit	2819 2183		
		Examiner Name	Unknown W. TREAT		
Sheet	1	of	4	Attorney Docket Number	X-841-1C US

U.S. PATENT DOCUMENTS						
Examiner Initials *	Cite No. *	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code (if known)				
WMT		US-	Re 3,363	08-03-93	Freeman	
		US-	4,314,353	02-02-82	Gunter et al.	
		US-	4,789,951	12-06-88	Birkner et al.	
		US-	4,870,302	09-26-89	Freeman	
		US-	5,093,915	03-03-92	Platteter et al.	
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		US-	5,448,493	09-05-95	Topolewski et al.	
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		US-	5,635,851	06-03-97	Tavana	
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FOREIGN PATENT DOCUMENTS							
Examiner Initials *	Cite No. *	Foreign Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T *
		Country Code *	-Number * -Kind Code * (if known)				
WMT			EP 0 062 431 A1	03-19-82	Fujitsu Limited		
↓			EP 0 139 254 A2	09-28-84	IBM		
			EP 0 307 649 A2	08-19-88	Compaq Computer Corp.		
			EP 0 306 962 A2	09-08-88	Hitachi, Ltd.		
			EP 0 361 525 A2	09-29-89	Hitachi, Ltd.		
			EP 0 486 248 A2	11-12-91	Xilinx, Inc.		
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			EP 0 536 793 A2	10-09-92	Kabushiki Kaisha Toshiba		
WMT			EP 0 636 976 A1	07-21-94	Philips Electronics N.V.		

Examiner Signature	W. TREAT	Date Considered	5/12/05
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OTHER - NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No *	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T *
WMT		IBM; Technical Disclosure Bulletin; "System Master Slice for Fast Turnaround Time"; Vol. 26, No. 3B; August 1983; pp. 1531-1532.	
		IBM; Technical Disclosure Bulletin; "Update Mechanism for Personal Computer System Resident Firmware"; Vol. 34, No. 10B; March 1992; pp. 133-136.	
		IBM; Technical Disclosure Bulletin; "Protocol Extensions to Microprocessor Memory Bus to Support Extend Extended Address Space"; Vol. 37, No. 05; May 1994; pp. 389.	
WMT		IBM; Technical Disclosure Bulletin; "Efficient Mechanism for Multiple Debug Modes"; Vol. 38, NO. 11; November 1995; pp. 65-68.	

Examiner
Signature

W. TREAT

Date
Considered

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